An Efficient Wavelet Based Filtered Multitone Modulation Scheme

Andrea M. Tonello¹ and Roman M. Vitenberg²

¹DIEGM - Università di Udine Via delle Scienze 208 - 33100 Udine - Italy phone: +39 0432 558 288 fax: +39 0432 558 251 e-mail: tonello@uniud.it ²DATA-JCE Ltd 21B, Habarzel St., 69710 - Tel Aviv - Israel phone: +972 3 6479966 fax: +972 3 647 7240 e-mail: roman@data-jce.com

Abstract—In this paper we describe a novel wavelet based filtered multitone modulation scheme for transmission over broadband wireless channels. The attractive feature of this scheme is its low implementation complexity compared to known filtered multitone modulation architectures that are based on polyphase synthesis and analysis filter banks. Its feasibility has been demonstrated by a prototype hardware implementation using FPGA technology that has been developed by DATA-JCE Ltd.

Keywords—Filtered multitone modulation (FMT), Multicarrier Modulation, OFDM, Hardware prototype, FPGA implementation.

I. Introduction¹

HIS paper deals with multicarrier modulation for application in broadband wireless transmission. Multicarrier modulation has the potentiality of simplifying the equalization task that becomes mandatory in wideband channels that experience severe frequency selectivity. Orthogonal frequency division multiplexing (OFDM) is probably among the most popular multicarrier modulation techniques. Herein, we refer to it as discrete multitone (DMT) modulation. It is essentially based on multicarrier transmission with sub-channel pulses that have a rectangular impulse response (Fig. 1). Its digital implementation comprises an M-point inverse fast Fourier transform (IFFT), where M equals the number of subchannels, followed by the insertion of a cyclic prefix. If the channel time dispersion is within the length of the cyclic prefix, simple one-tap equalization can be used. More general multicarrier schemes deploy sub-channel filters with time-frequency concentrated response (Fig. 1). Under certain conditions they can be implemented by using an IFFT followed by low-rate subchannel filtering (Fig. 2). These schemes are referred to as filtered multitone modulated systems (FMT) [1] and have been originally proposed for application in very high speed digital subscriber line transmission (VDSL). If the sub-channels have disjoint frequency response, i.e., do not overlap in frequency, it is possible to avoid the inter-carrier interference (ICI), and get low inter-symbol interference (ISI) that can be corrected with sub-channel equalization [2], [5], [7]. FMT modulation has the potentiality of achieving better spectral efficiency than OFDM yet requiring sufficiently simple equalization [3]. Further, the sub-channel spectral containment of FMT based systems makes them an interesting solution for application in asynchronous multiuser systems, i.e., uplink communications where the users received signals are subject to distinct carrier frequency offsets, and time misalignments [5], [6].

Although, FMT modulation can in principle provide better

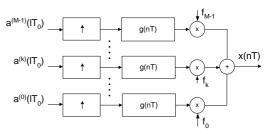


Fig. 1. Multitone modulator with "non efficient" implementation.

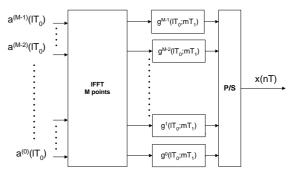


Fig. 2. Conventional synthesis of non-critically sampled FMT transmitter with an *M*-point core IFFT, and with $T_0 = NT$, $T_1 = MT$, $N' \ge M$, [1].

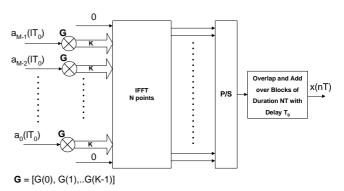


Fig. 3. Proposed frequency domain synthesis of the Wavelet FMT (WFMT) transmitter with an N-point core IFFT, and with $T_0 = (N/L)T$.

performance, lower out-of-band side lobs, better immunity to RF narrow band interference than DMT, its practical implementation is typically considered more complex than DMT. The polyphase architectures that are described in [1] are an efficient implementation but not necessarily a low (in the absolute sense) complexity one. In fact, the transmitter (synthesis filter bank)

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requires to run filtering with a bank of low rate filters at the output of the IFFT as shown in Fig. 2. The sub-channel pulses are obtained by the polyphase decomposition of a prototype pulse. Further, in a non-critically sampled FMT implementation, i.e., when the sub-channel data rate is lower than the sub-carrier spacing, the low-rate sub-channel pulses are periodic time-variant [1]. Similarly, the receiver front-end (analysis filter bank) comprises a bank of matched filters followed by a fast Fourier transform (FFT).

In this paper we propose and describe an efficient FMT scheme that is based on the deployment of wavelets. We refer to it as wavelet FMT (WFMT), Fig. 3. It should be noted that the design of the prototype pulse is of great importance both from a performance perspective and an implementation complexity point of view. The goal is to design a prototype pulse that has concentrated impulse and frequency response. The contribution is twofold. Firstly, we describe the theoretical aspects behind the scheme. Secondly, we report some details on the hardware implementation that has been done at DATA-JCE Ltd using FPGA technology.

The scheme comprises, at the transmitter side, the following stages: frequency domain synthesis of the sub-channel wavelet and data modulation. At the receiver side it comprises the analysis of the sub-channel wavelets, followed by simplified sub-channel equalization, and detection.

The prototype wavelet is designed to fulfil with the requirement of being time and frequency concentrated. We show that the wavelet can be synthesized via an IFFT with a small number *K* of non-zero coefficients.

The WFMT transmitter uses an N-point core IFFT and transmits M data streams a_i each at rate $1/T_0 = L/(NT)$. As shown in Fig. 3, the data streams modulate the frequency shifted wavelet coefficients. Some of the outermost frequencies can be set to zero for spectral containment reasons. The coefficients are passed through the IFFT, P/S converted and finally an overlap and add operation takes place to obtain the synthesized multicarrier signal. D/A conversion, and RF modulation is then implemented. At the receiver side, after RF demodulation, amplification, and A/D conversion, the received WFMT signal is processed by an analysis filter-bank that implements demodulation of the sub-channels wavelets. The analysis of the sub-channel wavelets is provided by the analysis of its frequency components by an FFT. To compensate the distortions introduced by the frequency selective fading channel we propose to use a simple equalization scheme. The overall receiver complexity is small.

II. PROTOTYPE WAVELET

Our multicarrier system deploys a prototype wavelet g(nT) where T is the transmission period (chip period). The prototype wavelet is designed to fulfil the requirements of being real, even, to have finite support NT, and to be orthogonal to its translations of multiples of $T_0 = NT/L$, for integers N and L, i.e.,

$$\kappa_{g}(nT_{0}) = g * g_{-}(nT_{0}) = \begin{cases} 1 & for \ n = 0 \\ 0 & for \ n \neq 0 \end{cases}$$
 (1)

From these constraints, it follows that the prototype wavelet can be obtained by a weighted sum of cosine components. We assume that the wavelet synthesis comprises only K cosine components. It can be shown that the value K is relatively small, and is a function of the level of inter-symbol interference (ISI) that is considered acceptable in the system design.

In Fig. 4 we show the level of inter-symbol interference (ISI) as a function of K for wavelets that were used for the realization of an efficient FMT algorithm. The minimal number of cosine components required for the prototype wavelet synthesis is 9.

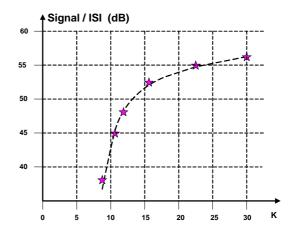


Fig. 4. Signal-to-interference power ratio as a function of the number *K* of wavelet frequency components.

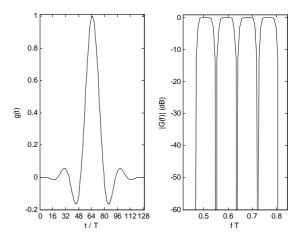


Fig. 5. Wavelet impulse response and frequency response for four adjacent sub-channels. Wavelet is generated using a 128 point core FFT with K=11 non-zero frequency components.

The practical FMT system that we developed uses a prototype wavelet constructed from K=11 components. It is shown in Fig. 5. Such a low number of frequency components give us the possibility to use an inverse discrete Fourier Transform (IDFT) (or its efficient version, the IFFT) for the prototype wavelet synthesis. We denote with G(k) = G(kF) the N-point DFT of g(nT) with F = 1/(NT). Note that with the above design the wavelet has bandwidth practically equal to KF. In other words the wavelet has K non-zero frequency components. Further, in our design we choose K = L + 3.

III. TRANSMITTER

The complex baseband transmitted signal is obtained by a filter bank modulator with prototype wavelet g(nT) and sub-channel carrier frequency

$$f_k = k_0 F + kKF$$
 $k = 0,...,M-1$ $MK \le N$ (2)

for an integer $0 \le k_0 < K$ that is chosen to set to zero the outermost frequencies. Therefore, it can be written as²

$$x(nT) = \sum_{k=0}^{M-1} \sum_{l=0}^{\infty} a^{(k)} (lT_0) g(nT - lT_0) e^{j2\pi f_k nT}$$
 (3)

where $a^{(k)}(lT_0)$ is the k-th sub-channel data stream that we assume to belong to the M-QAM constellation set.

 $^{^2\} T$ is assumed to be normalized to 1. The wavelet impulse response is assumed to be causal

A possible conventional efficient implementation that is based on polyphase filtering is shown in Fig. 2 [1]. However, by exploiting the fact that the wavelet can be synthesized with a small number K of frequency components, we can use an alternative efficient implementation as shown in Fig. 3. To derive it we can manipulate (3) to obtain

$$x(nT) = \sum_{l=-\infty}^{\infty} x_l(nT) \tag{4}$$

$$x_{l}(nT) = \sum_{k=0}^{M-1} a^{(k)} (lT_{0}) e^{j\frac{2\pi}{L}(Kk + k_{0})l} g(nT - lT_{0}) e^{j\frac{2\pi}{N}(Kk + k_{0})(n - \frac{N}{L}l)}$$
(5)

for n = lN/L,...,lN/L + N - 1, and zero otherwise. The finite time support signal (5) can be obtained by a *N*-point IDFT as

$$x_{l}(nT) = \frac{1}{N} \sum_{m=0}^{N-1} \left(\sum_{k=0}^{M-1} a^{(k)} (lT_{0}) e^{j\frac{2\pi}{L} (Kk+k_{0})l} G(m-kK-k_{0}) e^{j\frac{2\pi}{N} nm} \right)$$

$$= \frac{1}{N} \sum_{k=0}^{M-1} \sum_{m=0}^{K-1} a^{(k)} (lT_{0}) e^{j\frac{2\pi}{L} (Kk+k_{0})l} G(m) e^{j\frac{2\pi}{N} n(m+kK+k_{0})}$$
(6)

where G(m) is the m-th frequency component of the wavelet.

In conclusion as shown in Fig. 3, the M sub-channel data symbols $a_k(lT_0)=a^{(k)}(lT_0)e^{j2\pi(Kk+k_0)l/L}$ at a given instant lT_0 weight the frequency shifted wavelet components. Some zeros are added at the edges. Then, a N-point IDFT is computed. Finally, P/S conversion and an overlap and add operation, at rate $1/T_0$, take place. The overall data rate is equal to

$$R = \frac{ML}{NT} \,. \tag{7}$$

Note that the sub-channel expansion factor equals with our wavelet design K/L = K/(K-3), and decreases with an increasing number of wavelet frequency components K.

IV. CHANNEL MODEL

If we assume a discrete time channel model with complex tap amplitudes α_p , i.e., $h(nT) = \sum_{p=0}^{N_p} \alpha_p \delta(nT - pT)$, then the received signal in the absence of noise can be written as

$$y(nT) = \sum_{k=0}^{M-1} \sum_{l=-\infty}^{\infty} a^{(k)} (lT_0) e^{j\frac{2\pi}{L}(Kk+k_0)l} g_{EQ}^{(k)} (nT - lT_0)$$
 (8)

where $g_{EO}^{(k)}(nT)$ is the k-th sub-channel equivalent impulse response

$$g_{EQ}^{(k)}(nT) = \sum_{n=0}^{N_p} \alpha_p g(nT - pT) e^{j\frac{2\pi}{N}(Kk + k_0)(n-p)}$$
(9)

As it is often the case we assume the channel taps to be independent, zero mean, complex Gaussian (Rayleigh fading).

V. RECEIVER

The receiver front-end consists of a matched filter bank. Filters are matched to the equivalent sub-channel response [6]. The k-th filter output sample reads

$$z^{(k)}(mT_0) = e^{-j\frac{2\pi}{L}(Kk+k_0)m} \sum_{n} y(nT)g_{EQ}^{(k)*}(nT - mT_0) + \eta^{(k)}(mT_0)$$
 (10)

where $\eta^{(k)}(mT_0)$ is the additive Gaussian noise contribution. Further,

$$z^{(k)}(mT_0) = a^{(k)}(mT_0)\kappa_{EQ}^{(k)}(0)$$

$$+ \sum_{l \neq m} a^{(k)}(lT_0)e^{j\frac{2\pi}{L}(Kk+k_0)(l-m)}\kappa_{EQ}^{(k)}(mT_0 - lT_0) \qquad (11)$$

$$+ ICI^{(k)}(mT_0) + \eta^{(k)}(mT_0)$$

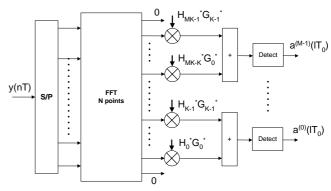


Fig. 6. Simplified matched filter receiver at instant lT_0 .

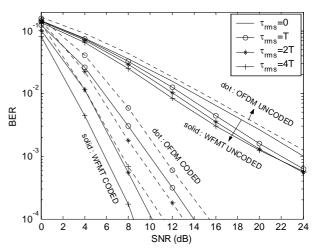


Fig. 7. Average BER in Rayleigh fading with exponential power delay profile with delay spread τ_{rms} . Both the WFMT scheme (solid curves) and an OFDM scheme (dotted curves) are compared. Both schemes use a 128 point FFT and have identical data rate. BPSK signalling is assumed with or without a rate ½ convolutional code.

where the first term represents the useful data contribution, the second additive term is the ISI contribution, the third term is the intercarrier interference (ICI) contribution. Further,

$$\kappa_{FO}^{(k)}(mT_0) = g_{FO}^{(k)} * g_{FO-}^{(k)*}(mT_0)$$
 (12)

is the equivalent sub-channel autocorrelation. If we assume frequency concentrated non-overlapping sub-channels the ICI term can be neglected.

To derive a simple detector we can assume that the prototype wavelet has short duration, such that convolved with the channel yields an equivalent impulse response with time support still equal to NT, i.e., $\kappa_{EQ}^{(k)}(mT_0) = 0$ for $m \neq 0$. Then, the matched filtering operation can take place in the frequency domain, as follows

$$z^{(k)}(mT_0) = e^{-j\frac{2\pi}{L}(Kk+k_0)m} \sum_{n=Kk+k_0}^{K(k+1)+k_0-1} Y_m(n) G_{EQ}^*(n)$$
 (13)

with

$$G_{EO}(n) \approx G(n - kK - k_0)H(n) \quad n \in k_0 + [kK, (k+1)K)$$
 (14)

$$Y_m(n) = DFT\{y(kT + mNT), k = 0,...,N-1\}$$
 $n = 0,...,N-1$ (15)

that is, $Y_m(n)$ is the DFT of the *m*-th received block of *N* samples, while $G_{EQ}(n)$ is the DFT of the equivalent channel response. This simplified matched filter receiver is shown in Fig. 6. A further simplification can be obtained by choosing, e.g., with a minimum

mean square error criterion, a single channel weight $H^{(k)}$ within a single sub-channel k, which corresponds to assume the channel flat within a bandwidth KF.

In Fig. 7 we show BER performance of the proposed WFMT scheme. We assume a Rayleigh faded channel with exponential power delay profile (truncated to 32 chips) with root-mean-square delay spread τ_{rms} . Further, we use BPSK signalling, and parameters N=128, K=11, L=8, M=11 with the wavelet in Fig. 5. If for instance the bandwidth is equal to 20 MHz, the delay spreads considered range from 50 ns up to 200 ns. We further consider channel coding with an interleaved convolutional encoder of rate $\frac{1}{2}$ and constraint length 5. The simple equalization scheme above is deployed under the assumption of knowing the channel.

For comparison we report also the BER performance of an OFDM (DMT) system that uses a 128 point FFT, a cyclic prefix of length 32, and data rate identical to the WFMT system. The same channel encoder is deployed for the OFDM system.

Fig. 7 shows that the BER performance of the proposed WFMT system is good even without channel coding. Some error floor appears at high SNRs and high delay spreads. However, performance can be improved with more powerful equalization [2], [6], [8]. Note also that some frequency diversity exploitation is possible also without coding since over a certain SNR region the BER improves as the delay spread increases despite the simple equalization scheme. On the contrary the uncoded OFDM scheme does not provide any diversity gain and further an SNR penalty (although small) is introduced because the conventional OFDM receiver is not the optimal matched filter receiver [7]. With coding there is a deep performance improvement in both the WFMT and the OFDM scheme that is more pronounced for high delay spreads as a result of higher frequency diversity [7]. However, the coded WFMT scheme shows superior performance than the coded OFDM scheme still having identical data rate and comparable complexity.

Note that performance can be improved in the WFMT by using a more powerful equalizer. Further, FMT schemes exhibit more robustness than OFDM to time/frequency offsets due to their spectral containment properties [3], [4]. Although we have not considered the synchronization and channel estimation problem in this paper, several approaches for general FMT architectures can be found in [4], [8].

VI. HARDWARE IMPLEMENTATION

The most significant issue of FMT implementation is its complexity. Several authors give different complexity estimation for FMT based on different architectures of poly-phase filters and equalizers. For correct estimation of the FMT chip complexity, size, and for verification of simulation results, an FPGA prototype of the proposed WFMT system was developed. The parameters of the prototype that has been developed at DATA-JCE have been chosen for VDSL applications but they can be adapted to wireless applications as considered in this paper and are an interesting example to understand what the overall complexity is. For the VDSL scenario, it is common to think that FMT technology is more complex than DMT, and that an FMT VDSL chip must comprise at least 2-4 times more transistors (gates) than a DMT VDSL chip. However, we have found that the proposed WFMT scheme can be implemented using a chip that has similar size and cost than a DMT chip.

The FMT system prototype includes a transmitter and receiver implemented in different FPGA VERTEX2 (3000) of Xilinx. An external processor ARM7 (ATMEL AT91) was used to control the equalizer. Because of FPGA limitations the system clock of 60 MHz was provided instead of 88 MHz that is needed in the

implemented VDSL operation mode B. As a result, the signal bandwidth was limited to 3 MHz instead of 4.4 MHz.

A 1024 point core FFT was used to obtain 44 sub-channels, 32 sub-channels for the downstream (138 kHz-3.7 MHz), and 8 for the upstream. The bandwidth of each FMT sub-channel is about 59 kHz. Constellations from 2 to 12 bit/symbol were used.

It is interesting to note that the WFMT chip complexity does not depend on the signal bandwidth provided that we keep the same core FFT size, and number of sub-channels. However, it is clear that the system clock changes for different operation modes, e.g., in VDSL it ranges from 88 MHz (mode A-B) up to 260 MHz (mode C) [9].

Both the transmitter and receiver tests have shown a close coincidence with the simulation results.

Details on the complexity of the FMT chip targeted for VDSL applications can be found in [8]. The complexity estimate accounts for 1007 KGates, and 162 KB of RAM, including transmitter, receiver, framer, and processor. For 0.18 μm technology the chip size may be calculated as:

 $S=(1007KG/80KG/mm^2+162KB/16.32KB/mm^2)*1.25$ $=(12.58mm^2+9.93mm^2)*1.25=28.2 mm^2.$

which proves that the scheme can be realized in a small silicon chip.

VII. CONCLUSIONS

We have presented a wavelet based multitone system and an alternative (compared to the filter bank approaches in [1]) efficient implementation. A simple equalization scheme has been described. Simulation performance results show that the scheme is robust to channel frequency selectivity yet requiring a simple detection algorithm. Although, we have not discussed it herein, the FMT schemes, and in particular the WFMT scheme of this paper, shows more robustness to carrier frequency offsets and timing errors than OFDM, and is capable of yielding higher spectral efficiency. An hardware prototype implementation targeted for VDSL applications has been done at DATA-JCE and it shows that the overall chip complexity is small. Current research activity includes the design and development of an hardware prototype for wireless applications.

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